

**(R24) I M.TECH II SEMESTER SUPPLE END EXAMINATIONS JUNE- 2026**

**TIME TABLE**

**TIME: 01:00 PM TO 04:00 PM**

DATE	CIVIL ENGG.	ELECTRICAL & ELECTRONICS ENGG.	MECHANICAL ENGG.	ELECTRONICS & COMMUNICATION ENGG.		COMPUTER SCIENCE & ENGG
	STRUCTURAL ENGINEERING	POWER AND INDUSTRIAL DRIVES	MACHINE DESIGN	DECS	VLSI & Embedded Systems	
29-06-2026	VALUE EDUCATION (AUDIT COURSE) (R24MCC2AC)	VALUE EDUCATION (AUDIT COURSE) (R24MCC2AC)	VALUE EDUCATION (AUDIT COURSE) (R24MCC2AC)	VALUE EDUCATION (AUDIT COURSE) (R24MCC2AC)	VALUE EDUCATION (AUDIT COURSE) (R24MCC2AC)	VALUE EDUCATION (AUDIT COURSE) (R24MCC2AC)
30-06-2026	FINITE ELEMENT METHODS IN STRUCTURAL ENGINEERING (R24MSE201)	CONTROL OF DC AND AC DRIVES (R24MPI201)	FINITE ELEMENT METHODS AND APPLICATIONS (R24MMD201)	IMAGE AND VIDEO PROCESSING (R24MDE201)	ANALOG & DIGITAL CMOS VLSI DESIGN (R24MVL201)	MACHINE LEARNING (R24MCS201)
01-07-2026	THEORY OF PLATES AND SHELLS (R24MSE202)	POWER SYSTEM DEREGULATION (R24MPI202)	EXPERIMENTAL STRESS ANALYSIS (R24MMD202)	WIRELES AND COMMUNICATIONS NETWORKS (R24MDE202)	REAL TIME OPERATING SYSTEMS (R24MVL202)	DIGITAL MARKETING (R24MCS202)
02-07-2026	STABILITY OF STRUCTURES (R24MSE203)	POWER QUALITY (R24MPI204)	CONDITION MONITORING (R24MMD205)	CMOS ANALOG & DIGITAL IC DESIGN (R24MDE203)	SYSTEM ON CHIP DESIGN (R24MVL204)	DATABASES AND MINING (R24MCS203)
03-07-2026	EARTHQUAKE RESISTANT DESIGN OF BUILDINGS (R24MSE207)	CUSTOM POWER DEVICES (R24MPI207)	MECHANICS OF COMPOSITE MATERIALS (R24MMD208)	DSP PROCESSORS & ARCHITECTURES (R24MDE207)	NETWORK SECURITY AND CRYPTOGRAPHY (R24MVL208)	CLOUD COMPUTING (R24MCS206)

**NOTE:**

- i.ANY OMISSION OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS, IMMEDIATELY.
- II.EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES,THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.
- III.THE HOD'S ARE REQUESTED TO INFORM THE EXAMINATION SECTION (AUTONOMOUS) ANY OTHER SUBSTITUTE SUBJECTS THAT ARE NOT INCLUDED IN THE ABOVE LIST IMMEDIATELY.



**CONTROLLER OF EXAMINATIONS**



**CHIEF CONTROLLER OF EXAMINATIONS**