

**(R16) III B.TECH I SEM SUPPLEMENTARY EXAMINATIONS MARCH / APRIL - 2024**

**TIME TABLE**

TIME : 2.00 PM TO 5.00 PM

BRANCH/DATE	22.03.2024	26.03.2024	28.03.2024	01.04.2024	03.04.2024	05.04.2024
CIVIL ENGINEERING (01- CE)	STRUCTURAL ANALYSIS-II (R16CE3101)	CONCRETE TECHNOLOGY (R16CE3102)	GEOTECHNICAL ENGINEERING-I (R16CE3103)	DESIGN AND DRAWING OF CONCRETE STRUCTURES (R16CE3104)	TRANSPORTATION ENGINEERING-I (R16CE3105)	PRINCIPLES OF WATER QUALITY MANAGEMENT (OPEN ELECTIVE-I) (R16CC310E11)
ELECTRICAL AND ELECTRONICS ENGINEERING (02 - EEE)	POWER SYSTEM TRANSMISSION LINES (R16EE3101)	POWER ELECTRONICS (R16EE3102)	ELECTRICAL MEASUREMENTS (R16EE3103)	MICROPROCESSOR AND MICROCONTROLLERS (R16EE3104)	SIGNALS AND SYSTEMS (R16EE3105)	RENEWABLE ENERGY SOURCES (PROFESSIONAL ELECTIVE-I) (R16EE3106)
MECHANICAL ENGINEERING (03 - ME)	METAL CUTTING AND MACHINE TOOLS (R16ME3101)	HEAT POWER ENGINEERING (R16ME3102)	MACHINE DYNAMICS AND VIBRATIONS (R16ME3103)	PRINCIPLES OF MACHINE DESIGN (R16ME3104)	OPERATIONS RESEARCH (R16ME3105)	INDUSTRIAL ELECTRONICS (OPEN ELECTIVE-I) (R16CC310E4)
ELECTRONICS AND COMMUNICATION ENGINEERING (04 - ECE)	-----	LINEAR AND DIGITAL IC APPLICATIONS (R16EC3101)	DIGITAL COMMUNICATIONS (R16EC3102)	ANTENNA AND WAVE PROPAGATION (R16EC3103)	COMPUTER ORGANIZATION AND MICROPROCESSORS (R16EC3104)	AUTOMOTIVE ELECTRONICS (OPEN ELECTIVE-I) (R16CC310E6)
COMPUTER SCIENCE AND ENGINEERING (05 - CSE)	-----	DESIGN AND ANALYSIS OF ALGORITHMS (R16CS3101)	OOAD THROUGH UML (R16CS3102)	COMPILER DESIGN (R16CS3103)	COMPUTER NETWORKS (R16CS3104)	NETWORK MANAGEMENT SYSTEMS (R16CS3108)/ SOFTWARE REQUIREMENTS AND ESTIMATION (R16CS3107)

**NOTE:**

- i. ANY OMISSION OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS, IMMEDIATELY.
- ii. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.
- iii. THE HODs ARE REQUESTED TO INFORM THE EXAMINATION SECTION (AUTONOMOUS) ANY OTHER SUBSTITUTE SUBJECTS THAT ARE NOT INCLUDED IN THE ABOVE LIST IMMEDIATELY.



CHIEF CONTROLLER OF EXAMINATIONS