



NARASARAOPETA ENGINEERING COLLEGE

(AUTONOMOUS)

Kotappakonda Road, Yellamanda (P.O), NARASARAOPET - 522 601, Guntur Dist. A.P.

Sponsored by Gayatri Educational Development Society, Narasaraopet.

Approved by AICTE, New Delhi & Permanently Affiliated to JNTUK, Kakinada. Code : 47

Twice Accredited by NBA & NAAC "A" Grade - ISO 9001 : 2008 Certified Institution.

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Dr. M. SREENIVASA KUMAR

M.Tech., Ph.D (UK), MISTE., FIE (I).

PRINCIPAL

Faculty Development Programme on

"RECENT TRENDS IN IC DESIGN USING MENTOR GRAPHICS EDA TOOLS"

Chief Patron

Sri M.S. Chakravarthi

B.E., M.S(USA), Exec-MBA(ISB)

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Sri M. Ramesh Babu

Secretary, NEC Group

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Dr. M. Sreenivasa Kumar

M.Tech., Ph.D. (UK), MISTE., FIE(I)

Principal.

Coordinator

Dr. V. Venkata Rao, Professor & Head

Co-coordinators

Dr. A.V. Nageswararao, Associate Professor

Dr. K. Sreenivasa Ravi, Professor

Dr. K. Raju, Associate Professor

Dr. B. Raghavaiah, Associate Professor

Organizing Committee

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Prof. P. S.S.Chakravarthi	Mr. G. Sai Ganesh
Mr. J. Narasimha Rao	Mrs. T. Indira
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Mr. M. Srinivasa Rao	Mr. Ch. Karthik
Mrs. Ch. Krishna Priya	Ms. K. Lakshmi Prasanna

INVITATION

Date: 11-11-2019

To

The Director/Principal

Dear Sir/Madam,

It gives us immense pleasure to inform you that, our college is organizing a Five Day Faculty Development Programme on ***"RECENT TRENDS IN IC DESIGN USING MENTOR GRAPHICS EDA TOOLS"*** by the Department of Electronics and Communication Engineering in association with CoreEL Technologies from **18th -22nd November, 2019**. The program will be conducted by esteemed faculty members from reputed Universities, Institutions and Industry.

Integrated circuits are one of the key complex units available to designers of multichannel detector setups. A whole number of factors makes Application Specific Integrated Circuits (ASICs) valuable for Particle Physics and Astrophysics experiments. Among them the most important ones are: integration scale, low power dissipation, radiation tolerance. In order to make possible future experiments in the intensity, cosmic, and energy frontiers today, ASICs should provide new level of functionality at a new set of constraints and trade-offs, like low-noise high-dynamic range amplification and pulse shaping, high-speed waveform sampling, low power digitization, fast digital data processing, serialization and data transmission. The aim of this FDP is to provide sound mathematical and theoretical concepts with hands on experience in this field, for the teaching and research community associated with the department of ECE.

We cordially invite your college faculty members to participate in this FDP. We deem it honour if your faculty members would join us in good number in this program. We request you to kindly give wide publicity by circulating the brochure enclosed among the faculty members of your college and encourage them to take part in this program.


We look forward for your cooperation and active participation of your faculty members.

For further details, contact:

Dr. V. Venkata Rao : 9441127485
Professor & Head, Dept. of ECE

Dr. A.V. Nageswara Rao : 9440664462
Associate Professor, ECE

With Regards


Dr. M. Sreenivasa Kumar
Principal