FDP ON

RECENT TRENDS IN IC DESIGN

USING

MENTOR GRAPHICS EDA TOOLS

18th – 22nd November, 2019

In Association with

CoreEL Technologies

Enabling Excellence

REGISTRATION FORM

Name: _____

Designation: _____

Mobile: _____

College Name: _____

E-Mail: _____

Area of Research: _____

Accommodation required: Yes/No

Registration Fee: **500/-**.

Signature of the Applicant

Note: The scanned copy of duly filled application form should be send to ecehod@nrtec.ac.in on or before 16-11-2019.

SPONSORSHIP CERTIFICATE

Dr./Mr./Ms._____
is working in our institution / Organization and is here by sponsored to participate in FDP on "***RECENT TRENDS IN IC DESIGN USING MENTOR GRAPHICS EDA TOOLS***" at Narasaraopeta Engineering College, Narasaraopet.

Topics to be covered:

- ✓ Basics of ASIC Design and ASIC Vs FPGAs
- ✓ Overview of IC Design with SOCs, MPSOCs and RFSOCs
- ✓ Current trends in Xilinx and Mentor Graphics Products and Features
- ✓ Full Custom Flow and Semi-Custom Design Flow
- ✓ Latest Advancements in VLSI Industry
- ✓ Lab-: Full Custom Design Flow
  Schematic Entry, Spice Models
  Simulation: DC Analysis and Measurement of Operation Point Analysis,
  Transient and Power Analysis
- ✓ Lab-: Layout entry
  Placement, Routing
  Physical Verification Using Industry Standard Calibre Tool
- ✓ Post Layout Simulation
- ✓ Lab: Amplifier Design and Analysis Simulation
  AC Analysis, Magnitude and Phase Plots
- ✓ Layout of Amplifier Placement & Routing
  Physical Verification

Impartment Dates:
Last Date for Registration:**15th November, 2019**
Date of Confirmation: **16th November, 2019**

Who Shall Participate:
The FDP is aimed at the multifaceted audience from industry, full time research scholars, PG students and academicians from various Engineering colleges of ECE.

Accommodation:
Limited Accommodation will be provided for outstation participants on prior request.
Contact for Accommodation: 9440664462

Note: As the seats are limited, the selection is based on first come first serve basis.

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Organized by

Department of Electronics & Communication Engineering

NEC

NARASARAOPETA ENGINEERING COLLEGE
(AUTONOMOUS)
Kotappakonda Road, Yellamanda (P.O)
Narasaraopet – 522601, Guntur District, A.P.
Approved by AICTE, Permanently Affiliated to JNTUK, Kakinada,
Accredited 'A' Grade by NAAC, Accredited by NBA..

Phone : **08647-239914**
FAX : **08647-239902**
E-Mail : **ecehod@nrtec.ac.in**

ABOUT THE COLLEGE

The Narasaraopeta Engineering College was established in the year 1998 by Gayatri Educational Development Society, Narasaraopet, under the leadership of Sri M.V.Koteswara Rao, who has a vision of developing this college as an autonomous institution of higher learning & knowledge in the heart of Palnadu region. The college is approved by AICTE, permanently affiliated to JNTUK Kakinada, Accredited 'A' Grade by NAAC and Accredited thrice by NBA. NEC is announced as 'AUTONOMOUS' in the year 2016. The college has launched Incubation Centre where the students can implement their ideas. The college has an MOU with Oracle University (USA). The college has AP CM Excellence centre, AI&ML centre and MSME centre. The college offers Seven M.Tech programmes, Two PG courses of MCA, MBA and Six courses at B.Tech level in the branches EEE, CSE, IT, ECE, ME and CIVIL. The college has well equipped laboratories and computers. Microsoft Innovation Centre was established in the college.

In order to build a vibrant entrepreneurship ecosystem Venture Development Centre (VDC) is established in association with APSSDC. Also, the college has Indo-European skilling Centre for Mechatronics and Industrial Robotics in collaboration with APSSDC and ARC (Applied Robot Control), Germany. AP CM's Skill Excellence Center is also established to provide students access to the most advanced courses by industry giants. The college has MOU with New Mexico State University, USA, which enables student and faculty exchange programs. The college is awarded AAA ranking by career 360 and also a member of TiE, Hyderabad. The college has established MHRD-IIC (Institution Innovation Council) and also a member of ICT Academy.

Narasaraopet is known as the gateway of Palnadu. Narasaraopet is a town in Guntur district and is on the railway line of Guntur to Guntakal. Narasaraopet is 50 Km from Guntur and 25 Km from Chilakaluripet (NH5) by road. Narasaraopet is 120 Km away from Vijayawada Airport. Kotappakonda is the site of major religious pilgrim place of Lord Siva which is located 5 Km from NEC.

ABOUT THE DEPARTMENT

The **Department of Electronics and Communication Engineering** was started in the year 1998 with an intake of 40 students in B. Tech. programme. The intake was enhanced subsequently and the present intake is 240. The department also offers two PG programmes, **M.Tech** (Digital Electronics and Communication Systems) which was started in the year 2004 with an intake of 18 and M.Tech (Digital Systems and Computer Electronics) which was started in the year 2006 with an intake of 18. The department has a team of highly qualified and experienced teaching faculty. The department has good infrastructural facilities and has full-fledged laboratories equipped with adequate state-of-the-art equipment and latest software. The UG programme of the department is accredited thrice by NBA. The department has Embedded Systems and Robotics Lab which was established under IIT Bombay E-Yantra Project. The department has vibrant atmosphere for the students to emphasize on fundamentals and concepts focusing on analysis and design of various applications to fulfill the needs of industry. The department encourages faculty members and research scholars to carry out their research.

ABOUT THE FDP

FDP on "***RECENT TRENDS IN IC DESIGN USING MENTOR GRAPHICS EDA TOOLS***" provides an excellent opportunity for faculty and researchers to learn new ideas in the field of VLSI, ranging from chip design to complete systems development & discovering novel tools for designing. The aim of FDP is to provide a platform, where researchers and academicians can get hands-on training and share their knowledge with each other and enhance their research skills. Faculty members are able to get deep insight into the details of advanced techniques in IC design. As a result, the faculty members participated in this program may propose new designs in this field.

RESOURCE PERSONS

1. Nagendra Bandi, Senior Application Engineer, CoreEL Technologies (I) Pvt Ltd, Bangalore
2. Dr. G Rama murthy, Professor, IIITH Hyderabad
3. Dr. Vaddi Ramesh, Associate Professor, Dept of

Chief Patron
Sri M.S. Chakravarthi
B.E., M.S(USA), Exec-MBA(ISB).
Vice - Chairman, NEC Group.

Patrons
Sri M.V. Koteswara Rao B.Sc.
Chairman, NEC Group.
Sri M. Ramesh Babu
Secretary, NEC Group.

Co-Patron
Dr. M. Sreenivasa Kumar
M.Tech., Ph.D. (UK), MISTE., FIE(I).
Principal.

Coordinator
Dr. V. Venkata Rao, Professor & Head

Co-coordinators
Dr. A.V. Nageswararao, Associate Professor
Dr. K. Sreenivasa Ravi, Professor
Dr. K. Raju, Associate Professor
Dr. B. Raghavaiah, Associate Professor

Organizing Committee

Prof. J. V. K Ratnam	Mrs. B. Suneetha
Prof. P .S.S.Chakravarthi	Mr. G. Sai Ganesh
Mr. J. Narasimha Rao	Mrs. T. Indira
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Mr. P. Bhagya Raju	Mr. N. Rajeev Reddy
Mr. Y. V. Lakshmaiah	Mr. V. Rama Krishna Reddy
Mr. Sk. Zuber Basha	Mr. Ch. Suresh Babu
Mr. N. Srinivasa Rao	Mr. E. Narendra
Mr. M. Srinivasa Rao	Mr. Ch. Karthik
Mrs. Ch. Krishna Priya	Ms. K. Lakshmi Prasanna